

3087 U.S. PTO
09/994683



Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. mt SCANNED <u>LR1</u> O.A. <u>JS</u>	PATENT DATE
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APPLICATION NO. 09/994683	CONT/PRIOR D F	CLASS <u>435</u> 438	SUBCLASS <u>730</u> 731	ART UNIT <u>2822</u> 2826	EXAMINER <u>Andrzej</u>
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APPLICANTS
Hideki Iwaki
Yutaka Taguchi
Tatsuyoshi Ogura

TITLE
Multi-level circuit substrate, method for manufacturing same and method for adjusting a characteristic impedance therefor

PTO-2040
12/99

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION									

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	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED _____	
	_____ (Primary Examiner) (Date)			ISSUE FEE Amount Due Date Paid	
	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER _____	

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